

ABSTRACT

An LCD driver (liquid crystal display driver) IC chip 10 has an internal semiconductor device circuit including an input circuit 11 for inputting data, random access memory (RAM) 12 as a memory section, a logic circuit 13 as a data processing section, and an output circuit 14 including a latch circuit and outputting signals, in connection with each other. A bump electrode 15 is provided so as to correspond to input and output pads. Each bump electrode 15 is arranged so as to lie above some transistor devices (not shown in the drawing) in the input circuit 11 or the output circuit 14 and is provided with an insulating interlayer (not shown in the drawing) therebetween.